

L Number	Hits	Search Text	DB	Time stamp
1	2	((("6009028") or ("5983374")).PN.	USPAT	2004/11/01 08:32
2	5479522	discontinuing near1 test near2 block\$1 near2 number near1 failure memory cell\$1 near2 block near3 reach\$2 near2 predetermined number indicating repair near2 memory near1 impossible	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/11/01 08:35
3	4731615	substitution near1 spare memory component\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/11/01 08:35
4	5378	(discontinuing near1 test near2 block\$1 near2 number near1 failure memory cell\$1 near2 block near3 reach\$2 near2 predetermined number indicating repair near2 memory near1 impossible) with (substitution near1 spare memory component\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/11/01 08:36
6	1156785	compar\$3 and (written logical value with read-out logical value)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/11/01 08:37
7	1713	((discontinuing near1 test near2 block\$1 near2 number near1 failure memory cell\$1 near2 block near3 reach\$2 near2 predetermined number indicating repair near2 memory near1 impossible) with (substitution near1 spare memory component\$1)) and (compar\$3 and (written logical value with read-out logical value))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/11/01 08:39
9	1556061	masking near1 test memory cell\$1 near2 address line\$1 near1 other block\$1 near2 tested	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/11/01 08:41
10	1357	((discontinuing near1 test near2 block\$1 near2 number near1 failure memory cell\$1 near2 block near3 reach\$2 near2 predetermined number indicating repair near2 memory near1 impossible) with (substitution near1 spare memory component\$1)) and (compar\$3 and (written logical value with read-out logical value)) and (masking near1 test memory cell\$1 near2 address line\$1 near1 other block\$1 near2 tested)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/11/01 08:43
11	1551023	discontinuing near1 test near2 block\$1 with uncorrectable memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/11/01 08:44
12	1354	((discontinuing near1 test near2 block\$1 near2 number near1 failure memory cell\$1 near2 block near3 reach\$2 near2 predetermined number indicating repair near2 memory near1 impossible) with (substitution near1 spare memory component\$1)) and (compar\$3 and (written logical value with read-out logical value)) and (masking near1 test memory cell\$1 near2 address line\$1 near1 other block\$1 near2 tested)) and (discontinuing near1 test near2 block\$1 with uncorrectable memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/11/01 08:45

13	3321862	bad block detection	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/11/01 08:45
14	5150816	bad address line detection	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/11/01 08:46
15	1234	(((((discontinuing near1 test near2 block\$1 near2 number near1 failure memory cell\$1 near2 block near3 reach\$2 near2 predetermined number indicating repair near2 memory near1 imposible) with (substitution near1 spare memory component\$1)) and (compar\$3 and (written logical value with read-out logical value))) and (masking near1 test memory cell\$1 near2 address line\$1 near1 other block\$1 near2 tested)) and (discontinuing near1 test near2 block\$1 with uncorrectable memory)) and (bad block detection)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/11/01 08:46
16	1221	((bad address line detection) and ((((discontinuing near1 test near2 block\$1 near2 number near1 failure memory cell\$1 near2 block near3 reach\$2 near2 predetermined number indicating repair near2 memory near1 imposible) with (substitution near1 spare memory component\$1)) and (compar\$3 and (written logical value with read-out logical value))) and (masking near1 test memory cell\$1 near2 address line\$1 near1 other block\$1 near2 tested)) and (discontinuing near1 test near2 block\$1 with uncorrectable memory)) and (bad block detection))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/11/01 08:46
17	13430	714/718 or 714/711 or 714/710 or 365/201 or 365/200	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/11/01 08:47
18	28	((bad address line detection) and ((((discontinuing near1 test near2 block\$1 near2 number near1 failure memory cell\$1 near2 block near3 reach\$2 near2 predetermined number indicating repair near2 memory near1 imposible) with (substitution near1 spare memory component\$1)) and (compar\$3 and (written logical value with read-out logical value))) and (masking near1 test memory cell\$1 near2 address line\$1 near1 other block\$1 near2 tested)) and (discontinuing near1 test near2 block\$1 with uncorrectable memory)) and (bad block detection))) and (714/718 or 714/711 or 714/710 or 365/201 or 365/200)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/11/01 08:47